Japan Display Inc.

FOR MESSRS : _____ DATE : Apr. 28th ,2026

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX20D200VM5BAA

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ACCEPTED BY: _____ PROPOSED BY: Oblack Tsai

2. RECORD OF REVISION

<table>
<tr><th>DATE</th><th>SHEET No.</th><th>SUMMARY</th></tr>
<tr><td>May 6,’14</td><td>7B64PS-2710-TX20D200VM5BAA-2
Page 10-1/2</td><td>10.1 FRONT VIEW
Correction :
Dimensions of BEZEL OPENING.</td></tr>
<tr><td rowspan="2">Sep. 5,’14</td><td>7B64PS-2703-TX20D200VM5BAA-3
Page 3-1/1</td><td>Change:
3.1 DISPLAY FEATURES
Power Consumption: 3.84W for backlight → 4.92W for backlight</td></tr>
<tr><td>7B64PS-2705-TX20D200VM5BAA-3
Page 5-2/2, 6-1/2</td><td>Change:
I_{LED}=320mA → I_{LED}=410mA</td></tr>
<tr><td>Jan. 5,’21</td><td>7B64PS-2713-TX20D200VM5BAA-4
Page 13-1/1</td><td>13. DESIGNATION of LOT MARK
Added :
<table>
<tr><th>REV. No</th><th>ITEM</th><th>REMARKS</th></tr>
<tr><td>A</td><td>-</td><td>-</td></tr>
<tr><td>B</td><td>Color Filter Supplier Changed</td><td>PCN0998</td></tr>
</table></td></tr>
<tr><td rowspan="3">Mar.01,’23</td><td>7B64PS-2701-TX20D200VM5BAA-5
Page 1-1/1</td><td rowspan="2">Company logo changed :
KOE JDI Group Kaohsiung Opto-Electronics Inc. → JDI Japan Display Inc.</td></tr>
<tr><td>7B64PS-2713-TX20D200VM5BAA-5
Page 13-1/1</td></tr>
<tr><td>All page</td><td>Company name changed:
From “KAOHSIUNG OPTO-ELECTRONICS INC.”
to “JDI Taiwan Inc. Kaohsiung Branch”</td></tr>
<tr><td rowspan="2">Apr.28,’26</td><td>7B64PS-2710-TX20D200VM5BAA-6
Page 10-2/2</td><td>10.2 REAR VIEW
Revised PCBA Rear Drawing</td></tr>
<tr><td>7B64PS-2713-TX20D200VM5BAA-6
Page 13-1/1</td><td>13. DESIGNATION of LOT MARK
Added REV. No B</td></tr>
<tr><td></td><td></td><td></td></tr>
</table>

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 8.0” WVGA of 16:9 format amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R(red), G(green), B(blue) sequentially. This display is RoHS compliant, and COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX20D200VM5BAA
Module Dimensions	189.0(W) mm x 122.0(H) mm x 10.2 (D) mm
LCD Active Area	174.0(W) mm x104.4(H) mm
Pixel Pitch	0.2175(W) mm x 0.2175 (H) mm
Resolution	800 x 3(RGB)(W) x 480(H) Dots
Color Pixel Arrangement	R, G, B Vertical Stripe
LCD Type	Transmissive Color TFT; Normally White
Display Type	Active Matrix
Number of Colors	262k Colors (6-bit RGB)
Backlight	Light Emitting Diode (LED)
Weight	220 g
Interface	LVDS; 20 pins
Power Supply Voltage	3.3V for LCD; 12.0V for backlight.
Power Consumption	0.89W for LCD; 4.92W for backlight.
Viewing Direction	Super Wide Version

4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V_{DD}	-0.5	5.0	V	-
Input Voltage of Logic	V_I	-0.5	5.0	V	Note 1
Operating Temperature	T_{op}	-30	80	°C	Note 2
Storage Temperature	T_{st}	-30	80	°C	Note 2
Backlight Input Voltage	V_{LED}	-	15	V	-

Note 1: The rating is defined for the signal voltages of the interface such as CLK and pixel data pairs.

Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:

- Background color, contrast and response time would be different in temperatures other than 25°C.
- Operating under high temperature will shorten LED lifetime.

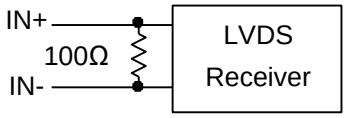
5. ELECTRICAL CHARACTERISTICS

5.1 LCD CHARACTERISTICS

$T_a = 25\,^{\circ}C,\ \mathrm{VSS} = 0\mathrm{V}$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-	3.0	3.3	3.6	V	-
Differential Input Voltage for LVDS Receiver Threshold	V_I	V_{IH}	-	-	+100	mV	Note 1
		V_{IL}	-100	-	-		
Power Supply Current	I_{DD}	V_{DD}-V_{SS} =3.3V	-	270	377	mA	Note 2,3
Frame Frequency	f_{Frame}	-	-	60	66	Hz	-
CLK Frequency	f_{CLK}	-	25	33.3	40	MHz	

Note 1: VCM 1.2V is common mode voltage of LVDS transmitter and receiver. The input terminal of LVDS receiver is terminated with 100Ω.



Note 2: An all black check pattern is used when measuring I_{DD}. f_{Frame} is set to 60Hz.

Note 3: 0.63A fuse is applied in the module for I_{DD}. For display activation and protection purpose, power supply is recommended larger than 1.6A to start the display and break fuse once any short circuit occurred.

5.2 BACKLIGHT CHARACTERISTICS

$T_a = 25\,^{\circ}C$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
LED Input Voltage	V_{LED}	-	11.0	12.0	13.0	V	Note1
LED Forward Current	I_{LED}	0V; 0% duty	360	410	460	mA	Note 2
		3.3VDC; 100% duty	30	40	50		
LED lifetime	-	I_{LED}=410mA	-	70K	-	hrs	Note 3

Note 1: As Fig. 5.1 shown, LED current is constant, 410mA, controlled by the LED driver when applying 12V V_{LED}.

Note 2: Dimming function can be obtained by applying DC voltage or PWM signal from the display interface CN1. The recommended PWM signal is 1K ~ 10K Hz with 3.3V amplitude.

Note 3: The estimated lifetime is specified as the time to reduce 50% brightness by applying 410mA at 25°C.

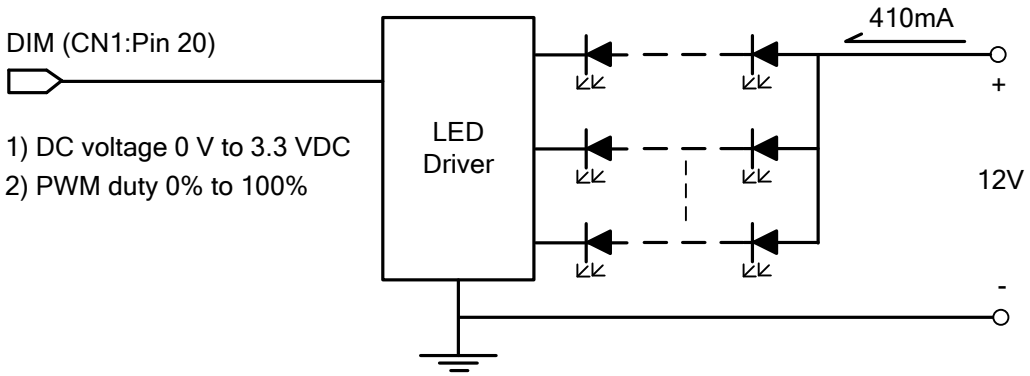


Fig 5.1

6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25°C.
- In the dark room less than 100 lx, the equipment has been set for the measurements as shown in Fig 6.1.

$T_a = 25\ ^{\circ}C,\ f_{Frame} = 60\ \text{Hz}, V_{DD} = 3.3\text{V}$

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Brightness of White		-	I_{LED}= 410mA $\phi = 0^{\circ}, \theta = 0^{\circ}$	800	1000	-	cd/m^2	Note 1
Brightness Uniformity		-		70	-	-	%	Note 2
Contrast Ratio		CR		200	400	-	-	Note 3
Response Time		Tr + Tf	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	20	-	ms	Note 4
NTSC Ratio		-	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	60	-	%	-
Viewing Angle		θ x	$\phi = 0^{\circ}$, CR ≥ 10	-	80	-	Degree	Note 5
		θ x′	$\phi = 180^{\circ}$, CR ≥ 10	-	80	-		
		θ y	$\phi = 90^{\circ}$, CR ≥ 10	-	80	-		
		θ y′	$\phi = 270^{\circ}$, CR ≥ 10	-	80	-		
Color Chromaticity	Red	X	$\phi = 0^{\circ}, \theta = 0^{\circ}$	0.59	0.64	0.69	-	Note 6
		Y		0.29	0.34	0.39		
	Green	X		0.31	0.36	0.41		
		Y		0.53	0.58	0.63		
	Blue	X		0.09	0.14	0.19		
		Y		0.05	0.10	0.15		
	White	X		0.26	0.31	0.36		
		Y		0.29	0.34	0.39		

Note 1: The brightness is measured from 9 point average value of the panel, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

$$\text{Brightness uniformity} = \frac{\text{Min. Brightness}}{\text{Max. Brightness}} \text{ X100\%}$$

which is based on the brightness values of the 9 points in active area measured by BM-5 as shown in Fig. 6.2.

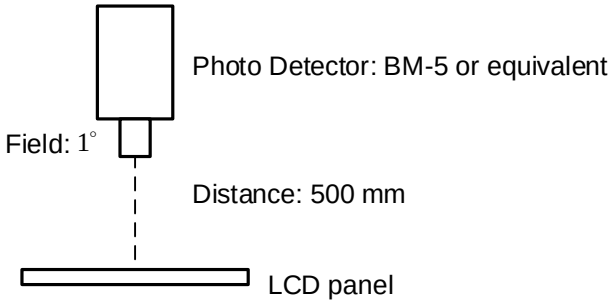


Fig 6.1

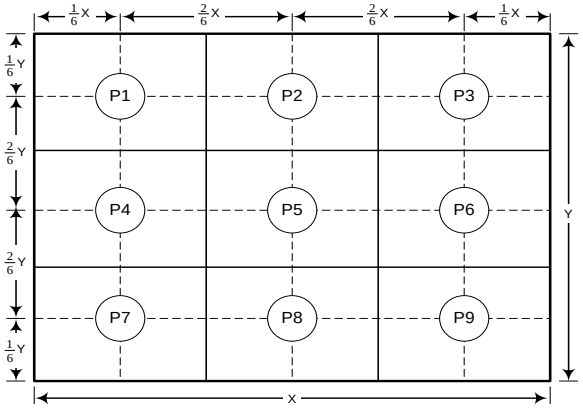


Fig 6.2

Note 3: The Contrast ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{\text{Brightness of White}}{\text{Brightness of Black}}$$

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 90% brightness to 10% brightness when the data is from white to black. Oppositely, falling time is the period from 10% brightness rising to 90% brightness.

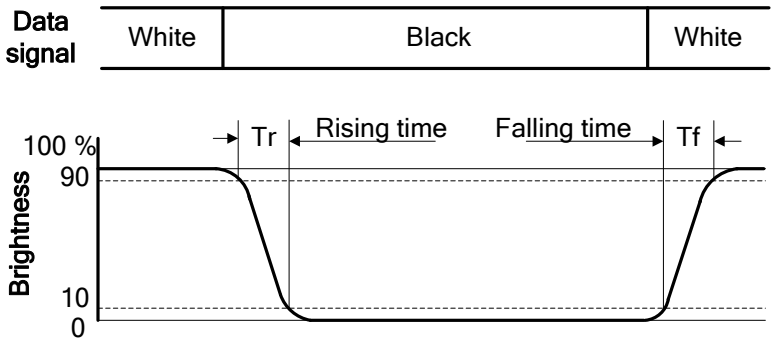


Fig 6.3

Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270^\circ$ means 6 o'clock, and $\phi = 0^\circ$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.

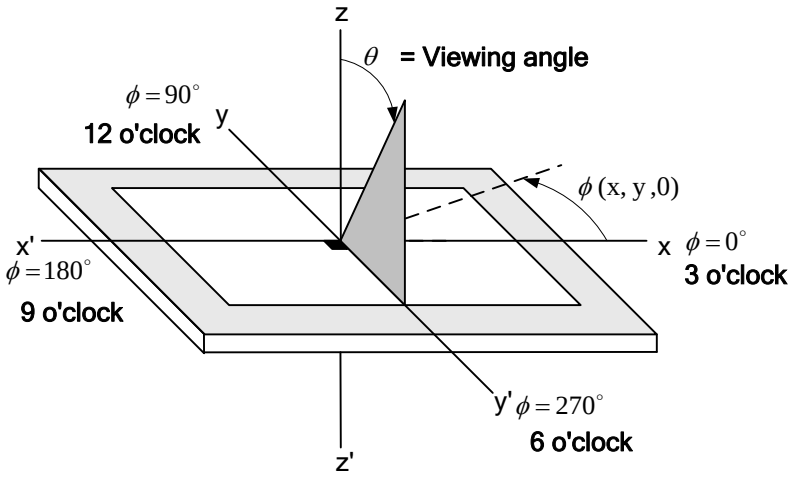
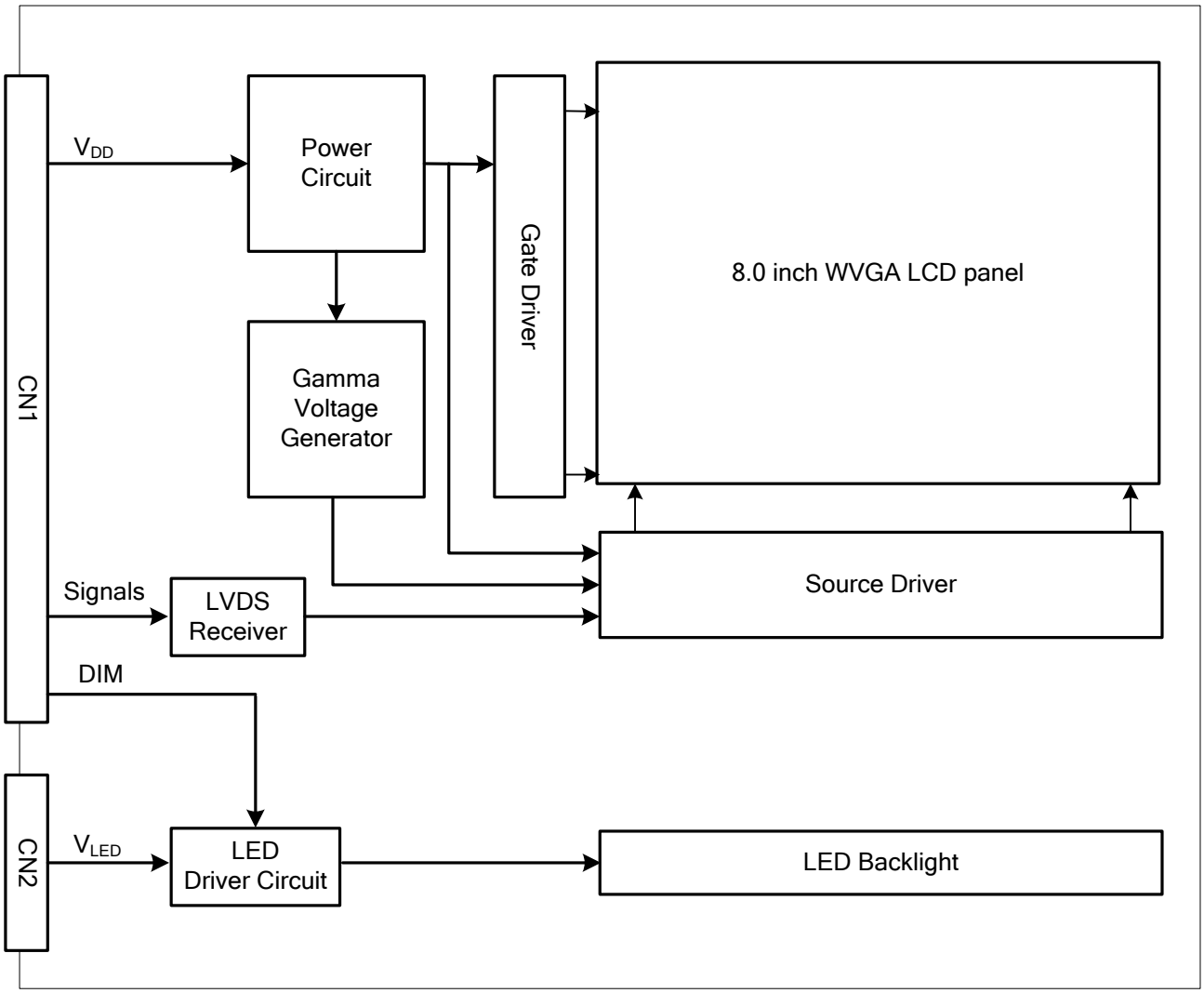


Fig 6.4

Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

7. BLOCK DIAGRAM



Note : Signals are CLK and pixel data pairs.

8. RELIABILITY TESTS

Test Item	Condition	
High Temperature	1) Operating 2) 80°C	240 hrs
Low Temperature	1) Operating 2) -30°C	240 hrs
High Temperature	1) Storage 2) 80°C	240 hrs
Low Temperature	1) Storage 2) -30°C	240 hrs
Heat Cycle	1) Operating 2) –20°C ~70°C 3) 3hrs~1hr~3hrs	240 hrs
Thermal Shock	1) Non-Operating 2) -35°C↔85°C 3) 0.5 hr↔0.5 hr	240 hrs
High Temperature & Humidity	1) Operating 2) 40°C & 85%RH 3) Without condensation	240 hrs (Note 3)
Vibration	1) Non-Operating 2) 20～200 Hz 3) 2G 4) X, Y, and Z directions	1 hr for each direction
Mechanical Shock	1) Non-Operating 2) 10 ms 3) 50G 4) ±X, ±Y and ±Z directions	Once for each direction
ESD	1) Operating 2) Tip: 150 pF, 330Ω 3) Air discharge for glass: ± 8KV 4) Contact discharge for metal frame: ± 8KV	1) Glass: 9 points 2) Metal frame: 8 points (Note 4)

Note 1: Display functionalities are inspected under the conditions defined in the specification after the reliability tests.

Note 2: The display is not guaranteed for use in corrosive gas environments.

Note 3: Under the condition of high temperature & humidity, if the temperature is higher than 40℃, the humidity needs to be reduced as Fig. 8.1 shown.

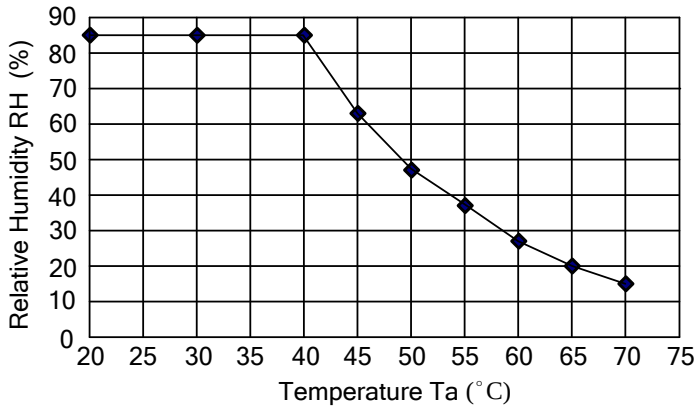


Fig. 8.1

Note 4: All pins of LCD interface (CN1) have been tested by ±100V contact discharge of ESD under non-operating condition.

9. LCD INTERFACE

9.1 INTERFACE PIN CONNECTIONS

The display interface connector CN1 is FI-SEB20P-HF13E made by JAE and pin assignment is as below:

Pin No.	Signal	Signal	Pin No.	Signal	Signal
1	V_{DD}	Power Supply for Logic	11	IN2-	B2~B5, DE
2	V_{DD}		12	IN2+	
3	V_{SS}	GND	13	V_{SS}	GND
4	V_{SS}		14	CLK IN-	Pixel Clock
5	IN0-	R0~R5, G0	15	CLK IN+	
6	IN0+		16	V_{SS}	GND
7	V_{SS}	GND	17	NC	No Connection
8	IN1-	G1~G5, B0~B1	18	NC	No Connection
9	IN1+		19	NC	No Connection
10	V_{SS}	GND	20	DIM	Note 2

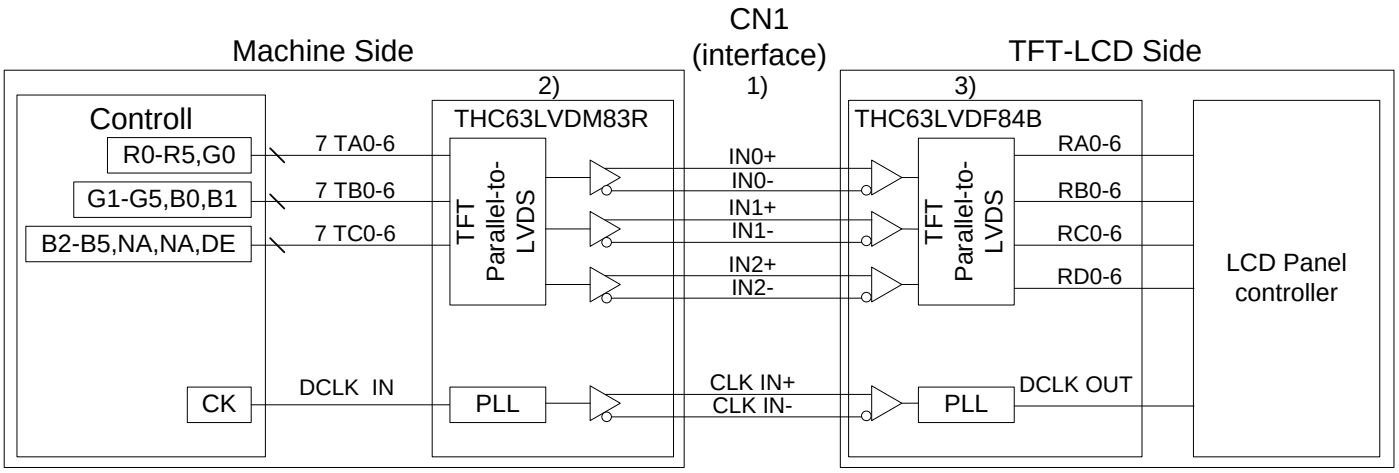
Note 1: IN n- and IN n+ (n=0, 1, 2), CLK IN- and CLK IN+ should be wired by twist-pairs or side-by-side FPC patterns, respectively.

Note 2: Normal brightness: 0V or 0% PWM duty; Brightness control: 0V to 3.3V DC or 0% to 100% PWM duty.

The backlight interface connector CN2 is SM02(8.0)B-BHS-1-TB made by JST, and pin assignment is as below:

Pin No.	Signal	Level	Function
1	V_{LED}+	-	Power Supply for LED
2	V_{LED}-	-	GND

9.2 LVDS INTERFACE

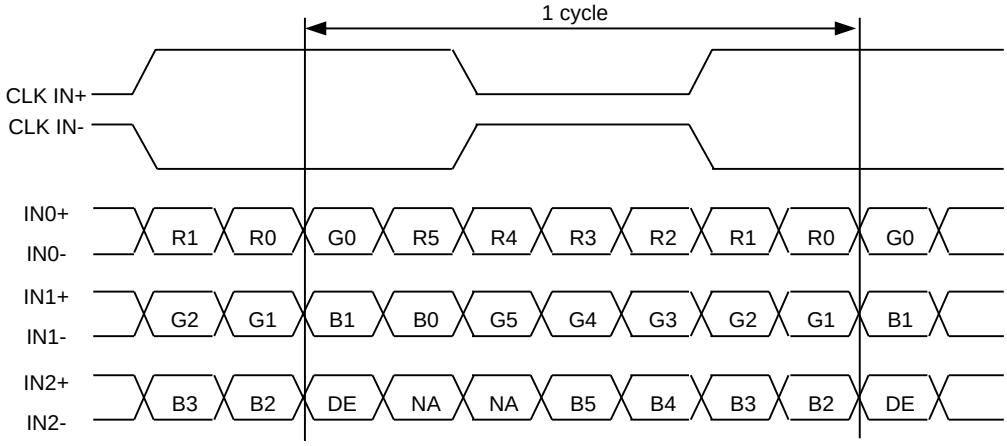


Note 1: LVDS cable impedance should be 100 ohms per signal line when each 2-lines (+, -) is used in differential mode.

Note 2: The recommended transmitter, THC63LVDM83R, is made by Thine or equivalent, which is not contained in the module.

Note 3: The receiver built-in the module is THC63LVDF84B .

9.3 LVDS DATA FORMAT



DE: Display Enable

NA: Not Available

9.4 TIMING CHART

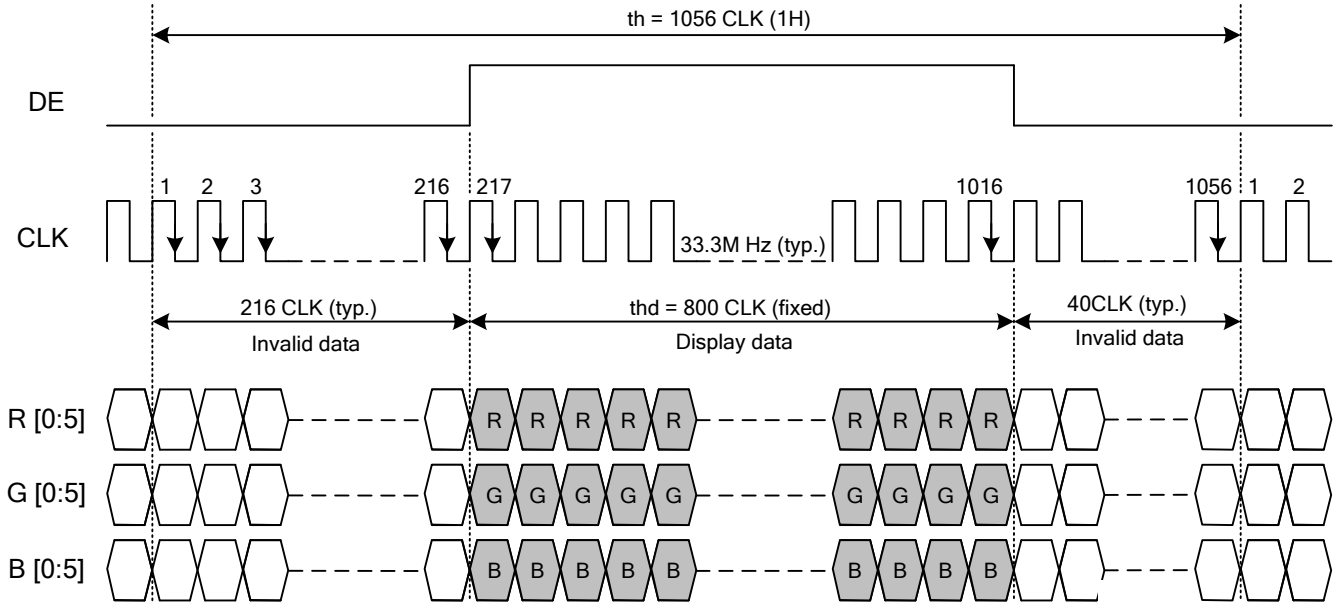


Fig. 9.1 Horizontal Timing

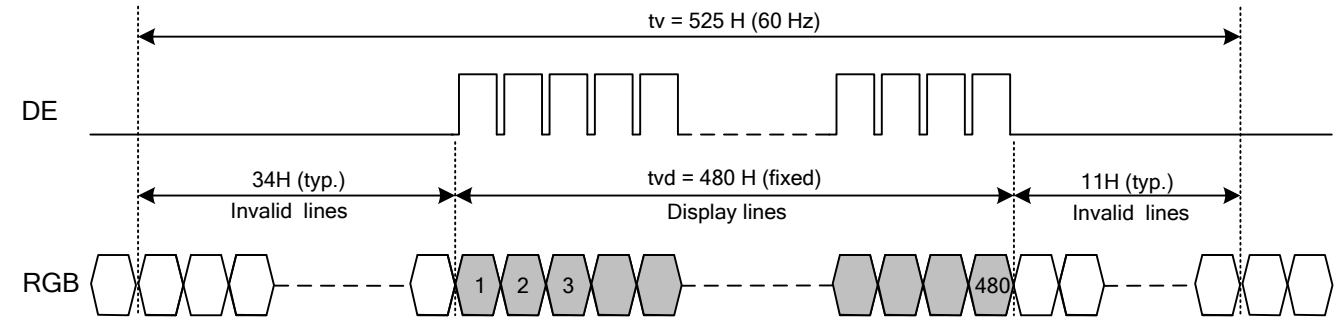


Fig.9.2 Vertical Timing

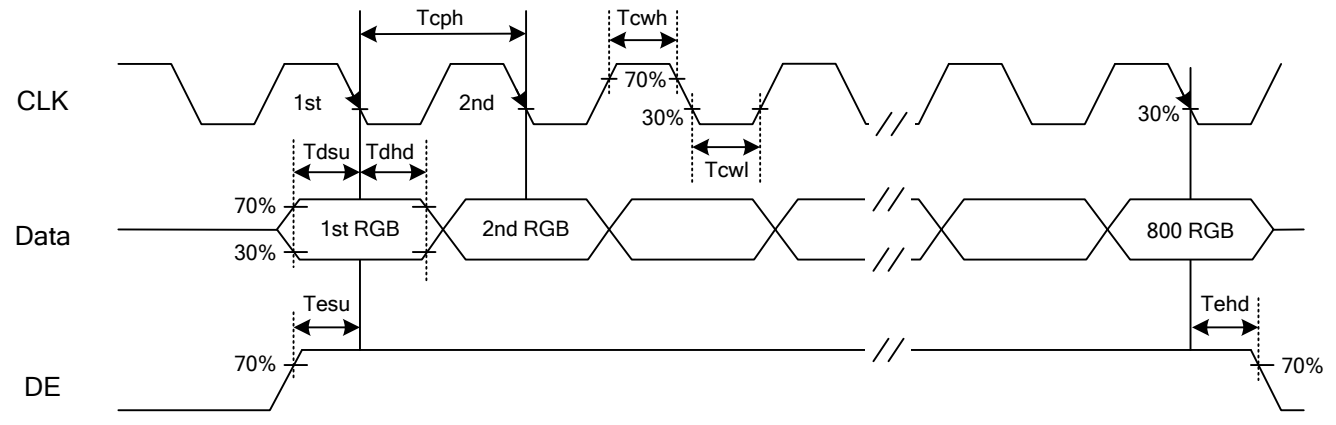


Fig. 9.3 Setup & Hold Time

9.5 TIMING TABLE

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (Vsync) = 60 Hz to define. If 60 Hz is not the aim to set, less than 66 Hz for Vsync is recommended to apply for better performance by other parameter combination as the definitions in section 5.1.

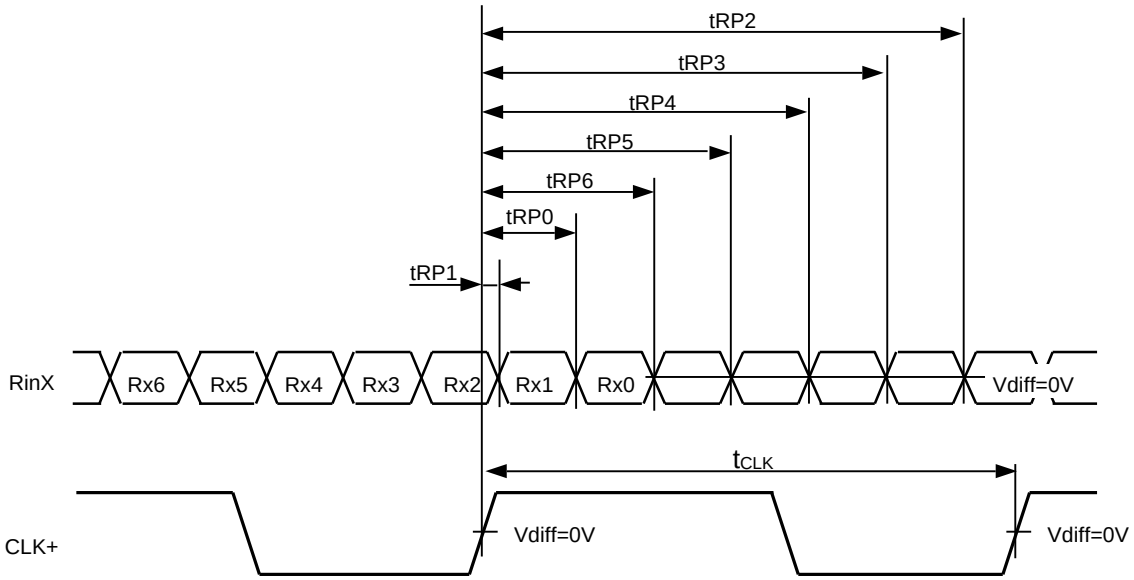
A. Horizontal and Vertical Timing

Item		Symbol	Min.	Typ.	Max.	Unit
Horizontal	CLK Frequency	fclk	25	33.3	40	M Hz
	Display Data	thd		800		CLK
	Cycle Time	th	841	1056	1088	
Vertical	Display Data	tvd		480		H
	Cycle Time	tv	495	525	610	

B. Setup and Hold Time

Item		Symbol	Min.	Typ.	Max.	Unit
CLK	Duty	Tcwh	40	50	60	%
	Cycle Time	Tcph	-	30	-	ns
Data	Setup Time	Tdsu	5	-	-	
	Hold Time	Tdhd	5	-	-	
DE	Setup Time	Tesu	5	-	-	
	Hold Time	Tehd	5	-	-	

9.6 LVDS RECEIVER TIMING



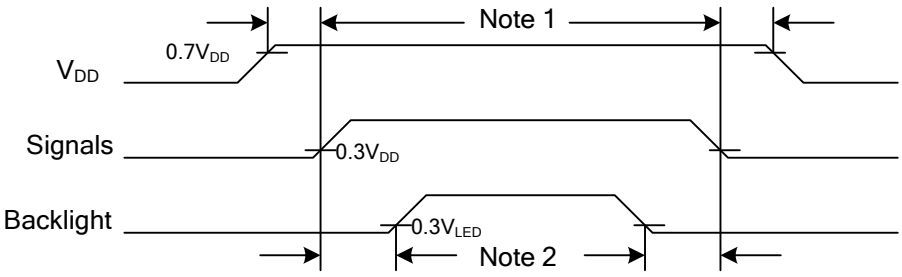
RinX= (RinX+)-(RinX-) (X=0, 1, 2)

Item		Symbol	Min.	Typ.	Max.	Unit
CLK	Cycle frequency	1/tcLK	25	33.3	40	MHz
RinX (X=0,1,2)	0 data position	tRP0	1/7* t_{CLK} -0.49	1/7* t_{CLK}	1/7* t_{CLK} +0.49	ns
	1st data position	tRP1	-0.49	0	+0.49	
	2nd data position	tRP2	6/7* t_{CLK} -0.49	6/7* t_{CLK}	6/7* t_{CLK} +0.49	
	3rd data position	tRP3	5/7* t_{CLK} -0.49	5/7* t_{CLK}	5/7* t_{CLK} +0.49	
	4th data position	tRP4	4/7* t_{CLK} -0.49	4/7* t_{CLK}	4/7* t_{CLK} +0.49	
	5th data position	tRP5	3/7* t_{CLK} -0.49	3/7* t_{CLK}	3/7* t_{CLK} +0.49	
	6th data position	tRP6	2/7* t_{CLK} -0.49	2/7* t_{CLK}	2/7* t_{CLK} +0.49	

9.7 DATA INPUT for DISPLAY COLOR

Input color		Red Data						Green Data						Blue Data					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green (2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

9.8 POWER SEQUENCE

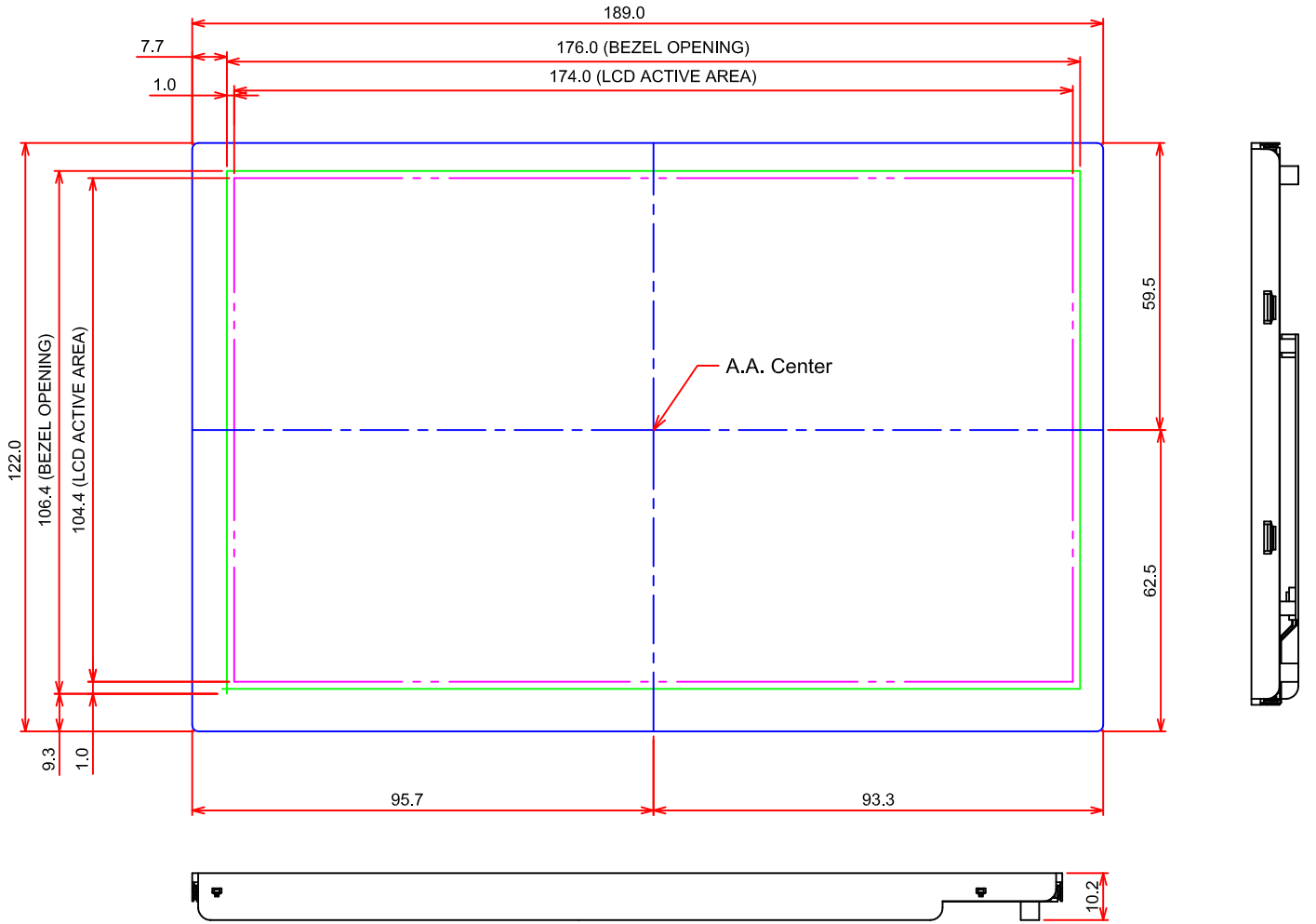


Note 1: In order to avoid any damages, V_{DD} has to be applied before all other signals. The opposite is true for power off where V_{DD} has to be remained on until all other signals have been switch off. The recommended time period is 1 second. Hot plugging might cause display damage due to incorrect power sequence, please pay attention on interface connecting before power on.

Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.

10. OUTLINE DIMENSIONS

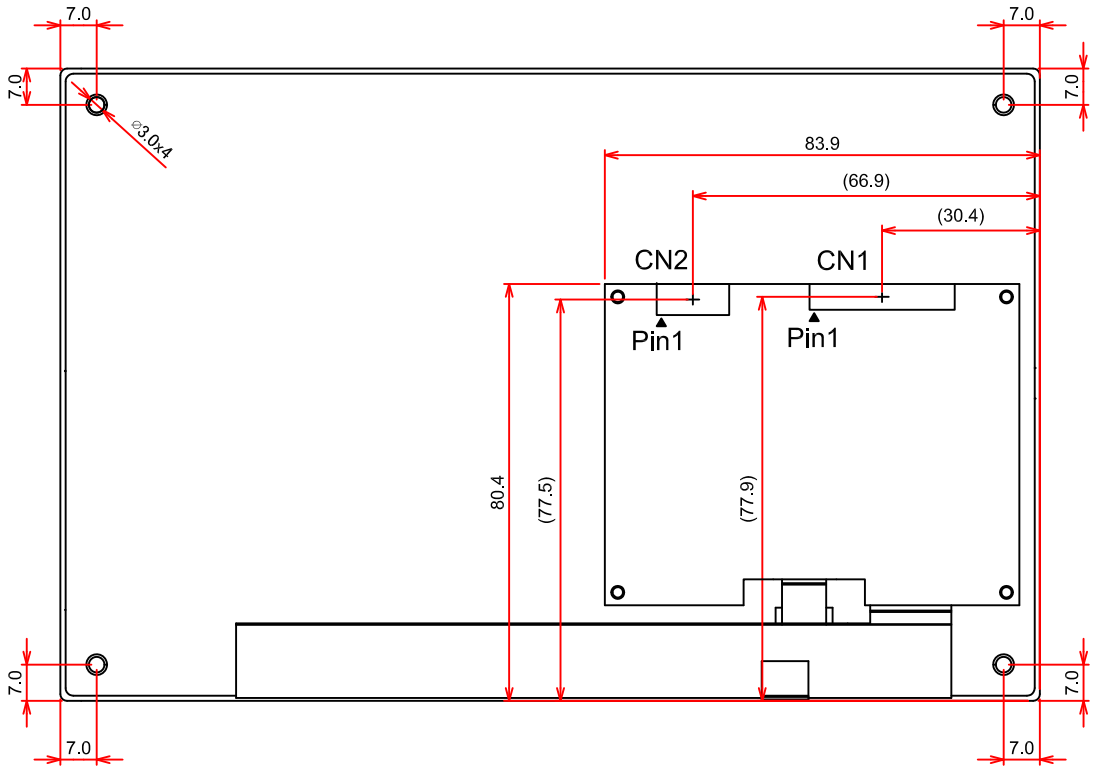
10.1 FRONT VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

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10.2 REAR VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

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11. APPEARANCE STANDARD

The appearance inspection is performed in a room around 500~1000 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle θ shown in Fig. 11.1 The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

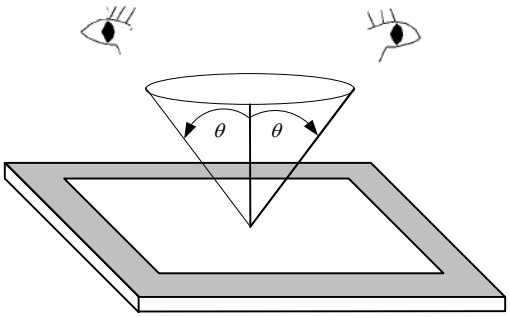


Fig. 11.1

11.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 2 areas as shown in Fig.11.2 for appearance specification in next section. A zone is the LCD active area (dot area); B zone is the area between A zone and metal frame.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

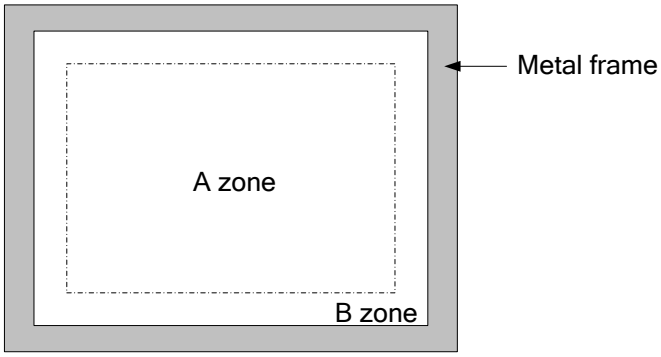


Fig. 11.2

11.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 11.4 and Fig. 11.5.

<table>
<tr><td>Item</td><td colspan="5">Criteria</td><td>Applied zone</td></tr>
<tr><td rowspan="4">Scratches</td><td>Length (mm)</td><td>Width (mm)</td><td colspan="2">Maximum number</td><td>Minimum space</td><td rowspan="4">A</td></tr>
<tr><td>L≦15</td><td>W≦0.02</td><td colspan="2">Ignored</td><td>-</td></tr>
<tr><td>L≦15</td><td>0.02＜W≦0.1</td><td colspan="2">5</td><td>-</td></tr>
<tr><td>L＞15</td><td>0.1＜W</td><td colspan="2">0</td><td>-</td></tr>
<tr><td>Dent</td><td colspan="5">Serious one is not allowed</td><td>A</td></tr>
<tr><td>Wrinkles in polarizer</td><td colspan="5">Serious one is not allowed</td><td>A</td></tr>
<tr><td rowspan="4">Bubbles on polarizer</td><td colspan="3">Average diameter (mm)</td><td colspan="2">Maximum number</td><td rowspan="4">A</td></tr>
<tr><td colspan="3">D＜0.3</td><td colspan="2">Ignored</td></tr>
<tr><td colspan="3">0.3≦D≦0.6</td><td colspan="2">4</td></tr>
<tr><td colspan="3">0.6＜D</td><td colspan="2">0</td></tr>
<tr><td rowspan="11">1) Stains
2) Foreign Materials
3) Dark Spot</td><td colspan="4">Filamentous (Line shape)</td><td rowspan="3">A</td></tr>
<tr><td>Length (mm)</td><td colspan="2">Width (mm)</td><td>Maximum number</td></tr>
<tr><td>L≦2.0</td><td colspan="2">W≦1.5</td><td>5</td></tr>
<tr><td>L＞2.0</td><td colspan="2">1.5＜W</td><td>0</td></tr>
<tr><td colspan="4">Round (Dot shape)</td><td rowspan="6">A</td></tr>
<tr><td>Average diameter (mm)</td><td colspan="2">Maximum number</td><td>Minimum Space</td></tr>
<tr><td>D＜0.2</td><td colspan="2">Ignored</td><td>-</td></tr>
<tr><td>0.2≦D＜0.6</td><td colspan="2">4</td><td>-</td></tr>
<tr><td>0.6≦D</td><td colspan="2">0</td><td>-</td></tr>
<tr><td colspan="4">Those wiped out easily are acceptable</td></tr>
<tr><td rowspan="4">Dot-Defect
(Note 1,2)</td><td></td><td>Area①</td><td>Area②</td><td colspan="2">Maximum number</td><td rowspan="4">A</td></tr>
<tr><td>Bright dot-defect</td><td>1 dot</td><td>2 dot</td><td colspan="2">3 dot</td></tr>
<tr><td>Dark dot-defect</td><td>2 dot</td><td>3 dot</td><td colspan="2">4 dot</td></tr>
<tr><td>Bright + Dark point</td><td>3 dot</td><td>4 dot</td><td colspan="2">5 dot</td></tr>
</table>

Note 1: The Dot-Defect inspection within A zone (active area) would be divided into area ①, ② as Fig. 11.3 shown.

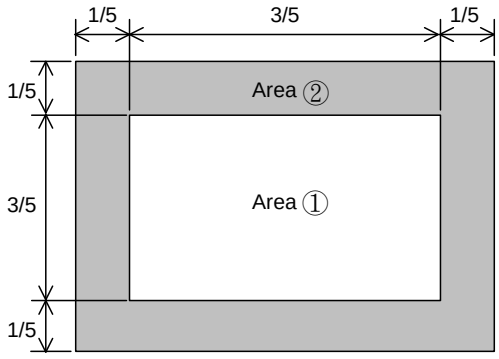


Fig. 11.3

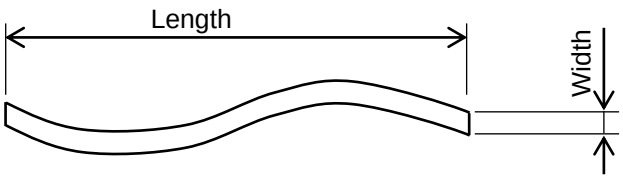


Fig 11.4

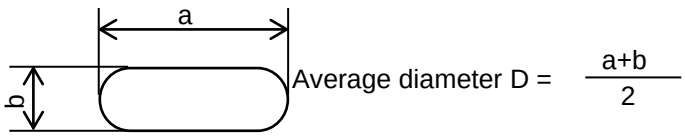


Fig 11.5

Note 2: The definitions of dot defect are as below:

- The defect area of the dot must be bigger than half of a dot.
- For bright dot-defect, showing black pattern, the dot's brightness must be over 30% brighter than others.
- For dark dot-defect, showing white pattern, the dot's brightness must be under 70% darker than others.

12. PRECAUTIONS

12.1 PRECAUTIONS of ESD

1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.

2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

12.2 PRECAUTIONS of HANDLING

1) In order to keep the appearance of display in good condition; please do not rub any surfaces of the displays by sharp tools harder than 3H, especially touch panel, metal frame and polarizer.

2) Please do not pile the displays in order to avoid any scars leaving on the display. In order to avoid any injuries, please pay more attention for the edges of glasses and metal frame, and wear finger cots to protect yourself and the display before working on it.

3) Touching the display area or the terminal pins with bare hand is prohibited. This is because it will stain the display area and cause poor insulation between terminal pins, and might affect display's electrical characteristics furthermore.

4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.

5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.

6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanently damages.

7) Maximum pressure to the surface of the display must be less than $1{,}96 \times 10^4$ Pa. If the area of adding pressure is less than $1\,cm^2$, the maximum pressure must be less than 1.96N.

12.3 PRECAUTIONS OF OPERATING

1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.

2) When the display is operating at significant low temperature, the response time will be slower than it at $25\,C^\circ$. In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.

3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.

4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than $\pm$ 100 mV.

12.4 PRECAUTIONS of STORAGE

If the displays are going to be stored for years, please be aware the following notices.

1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.
2) The recommended long-term storage temperature is between 10℃~35℃ and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.
3) It would be better to keep the displays in the container, which is shipped from JDI, and do not unpack it.
4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

13. DESIGNATION of LOT MARK

1) The lot mark is showing in Fig.13.1. First 4 digits are used to represent production lot, T represented product of JDI Taiwan, and the last 6 digits are the serial number.

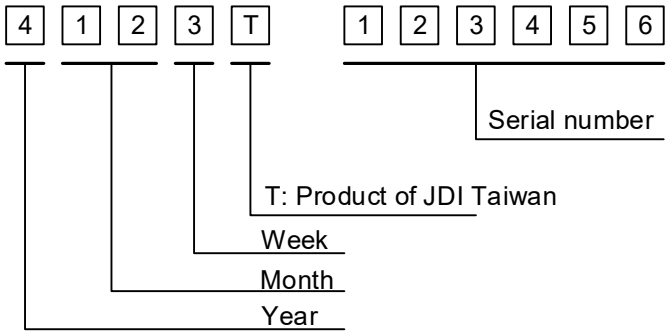


Fig. 13.1

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Lot Mark
2014	4
2015	5
2016	6
2017	7
2018	8

Month	Lot Mark	Month	Lot Mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Lot Mark
1~7 days	1
8~14 days	2
15~21 days	3
22~28 days	4
29~31 days	5

3) Except letters I and O, revision number will be shown on lot mark and following letters A to Z.

REV No.	ITEM	REMARKS
A	-	-
B	Color Filter Supplier Changed	PCN0998
B	Internal-use Connector Changed	PCN1119

4) The location of the lot mark is on the back of the display shown in Fig. 13.2.

Label example:



Fig. 13.2